

# ADVANCED

overall tree  
= binary tree of mini trees

mini trees

micro trees

actual nodes

$\frac{1}{4} \lg n$  nodes



# DATA STRUCTURES

# 6

## Succinct Data Structures

Advanced Data Structures · Summer 2026

Prof. Dr. Sebastian Wild

## 6 Succinct Data Structures

6.1 A Motivating Example

6.2 Bitvectors

6.3 Supporting Rank

6.4 Supporting Select

6.5 Compressed Bitvectors

6.6 Sequences

6.7 Trees

6.8 Graphs

## 6.1 A Motivating Example

# Computing over compressed data

- ▶ traditionally:
  - ▶ compression for **archiving** data, minimize size of representation
  - ▶ computation/analysis: minimize time; use extra data structures
  - ↪ always decompress data first!
- ▶ reaches limits of fast memory for large datasets

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- ▶ traditionally:
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  - ↪ always decompress data first!
  - ▶ reaches limits of fast memory for large datasets
- ▶ Approach in space-efficient data structures:
  - ▶ Represent data in compressed form
  - ▶ Augment with **small** index data structures to enable fast queries **directly on compressed representation**
  - ▶ succinct =  $(1 + o(1)) \cdot$  information-theoretic lower bound

$n = |U|$        $\lg n$  bits needed to distinguish objects in  $U$

# Computing over compressed data

$T[0..n) =$

|   |   |   |   |   |   |   |   |   |   |    |    |    |    |    |    |    |
|---|---|---|---|---|---|---|---|---|---|----|----|----|----|----|----|----|
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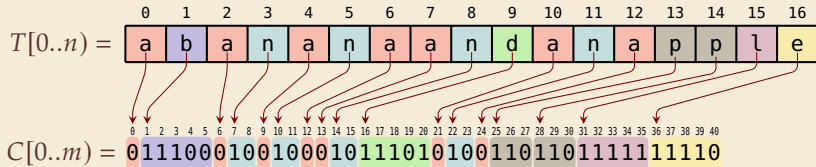
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a  $\mapsto$  0  
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|   |   |   |   |   |   |   |   |   |   |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |   |   |
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| 0 | 1 | 1 | 1 | 0 | 0 | 0 | 1 | 0 | 0 | 1  | 0  | 0  | 0  | 1  | 0  | 1  | 1  | 1  | 0  | 1  | 0  | 1  | 0  | 0  | 1  | 1  | 0  | 1  | 1  | 0  | 1  | 1  | 1  | 1  | 1  | 1  | 1  | 1  | 1  | 1  | 1 | 0 |

  
|  
c      b

► Can decode from beginning  $\rightsquigarrow$  fine for storage / transmission

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| 0 | 1 | 1 | 1 | 0 | 0 | 0 | 1 | 0 | 0 | 1  | 0  | 0  | 0  | 1  | 0  | 1  | 1  | 1  | 0  | 1  | 0  | 1  | 0  | 0  | 1  | 1  | 0  | 1  | 1  | 0  | 1  | 1  | 1  | 1  | 1  | 1  | 1  | 1  | 1  | 1  | 1 | 1 | 0 |

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| 0 | 1 | 1 | 1 | 0 | 0 | 0 | 1 | 0 | 0 | 1  | 0  | 0  | 0  | 1  | 0  | 1  | 1  | 1  | 0  | 1  | 0  | 1  | 0  | 0  | 1  | 1  | 0  | 1  | 1  | 0  | 1  | 1  | 1  | 1  | 1  | 1  | 1  | 1  | 1  | 1  | 1 | 0 |

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- ▶ We don't. But we can store it!
    - ▶ Naive way: Store starting index for each char
- $\rightsquigarrow n$  numbers in  $[n]$   $\rightsquigarrow n \lg n$  bits.

Much more than the (compressed) text!



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1 1 0 0 0 0 1 1 0 1  
     $\leftarrow$  select(9)

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Clever **succinct index** supports random access in constant time with  $o(n)$  extra bits!

## Huffman Code

|   |                 |
|---|-----------------|
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| d | $\mapsto$ 11101 |
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| l | $\mapsto$ 11111 |
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# Dynamic Bitvectors

Recall: Dynamic rank/select lower bounds (Fredman-Saks)

$$\Omega\left(\frac{\lg n}{\lg \lg n}\right) \text{ cell probes}$$

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Note: Can use bitvector to represent  $n$  numbers from universe  $[0..u)$

↪ Cannot have dynamic bitvectors **and** fast rank/select queries,  $\Omega\left(\frac{\lg n}{\lg \lg n}\right)$ .

↪ Focus here on static bitvectors.

## 6.2 Bitvectors

# Bitvector Representation in C++

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  - ▶ easy to access
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- ↪ in practice: `sdsl::bit_vector` (based on packing bits to words)
- ▶ from external library SDSL Lite
  - ▶ <https://github.com/simongog/sdsl-lite>

## Rank & Select on Bitvectors

- ▶  $B[0..n)$  static array of  $n$  bits (Boolean array).
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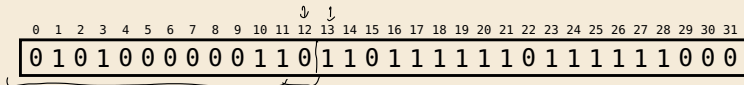
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= index of  $r$ th 1 in  $B$ , ( $r = 1, 2, \dots$ )

0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31  
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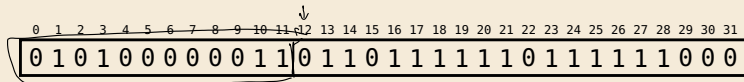
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**Goal:** Support rank and select on bitvector using  $n + \underline{o(n)}$  bits of space.

# Versatile Bitvectors

## ► Set of integers

►  $S \subset [0..u) \rightsquigarrow B[0..u)$  with  $B[x] = 1 \iff x \in S$

$\rightsquigarrow B.\text{select}(B.\text{rank}(x+1)) = S.\text{predecessor}(x)$

$\rightsquigarrow$  efficient intersection, union, etc. via bitwise logical operations

► used, e.g., for “Roaring Bitmaps”: one bitvector / integer set per attribute

## ► Unary encoded integers

► integers  $x_1, \dots, x_k \in \mathbb{N}_{\geq 0} \rightsquigarrow B = 1^{x_1}0 1^{x_2}0 \dots 1^{x_k}0$

►  $x_1 + \dots + x_j = B.\text{rank}(B.\text{select}_0(j))$

►  $x_j$  as difference of prefix sums

## ► Sparse array index $n \ll u$

$$n \ll k \cdot \log_2 u + o(u)$$

► suppose  $A[0..n)$  is mostly 0, but with some  $k$  indices having non-zero values

$\rightsquigarrow$  Store in  $V[0..k)$  all non-zero values compactly, in  $B[0..n)$  store  $B[i] = 1 \iff A[i] \neq 0$

$\rightsquigarrow$  Simulate  $A[x] = \begin{cases} 0 & \text{if } B[x] = 0 \\ V[B.\text{rank}[x]] & \text{else} \end{cases}$

► **Note:** In all these cases, rather natural to expect skewed frequencies of 1s and 0s.

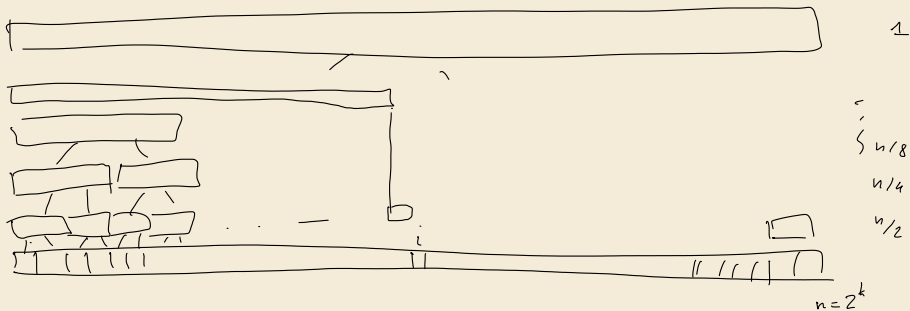
## 6.3 Supporting Rank

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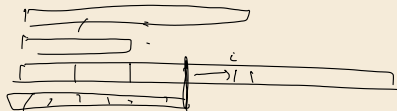
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## ▶ hybrid: **subsampling segment tree**

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- ▶ within block use linear scan
- ▶ entire blocks covered by segment tree ↪  $O(\log n)$  query time

▶ space:  $\Theta\left(\frac{n}{\lg n} \cdot \lg n\right) = \Theta(n)$  bits of extra space

↪ a bit better, but still neither succinct, nor constant time!  
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*How can we do better?*

# Rank Index for Bitvector

- ▶ Apart from  $B$ , we store:

$B$ 

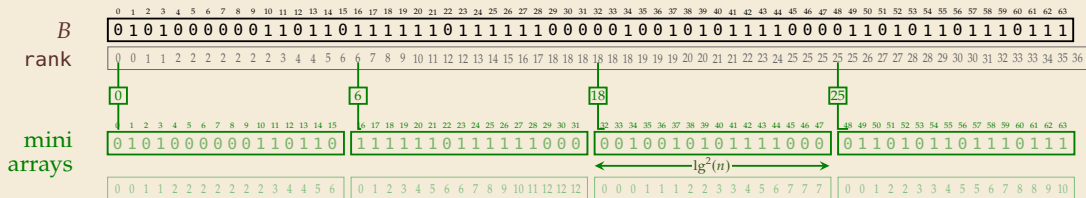
|   |   |   |   |   |   |   |   |   |   |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |
|---|---|---|---|---|---|---|---|---|---|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|
| 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 10 | 11 | 12 | 13 | 14 | 15 | 16 | 17 | 18 | 19 | 20 | 21 | 22 | 23 | 24 | 25 | 26 | 27 | 28 | 29 | 30 | 31 | 32 | 33 | 34 | 35 | 36 | 37 | 38 | 39 | 40 | 41 | 42 | 43 | 44 | 45 | 46 | 47 | 48 | 49 | 50 | 51 | 52 | 53 | 54 | 55 | 56 | 57 | 58 | 59 | 60 | 61 | 62 | 63 |
| 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 1  | 1  | 0  | 1  | 1  | 0  | 1  | 1  | 1  | 1  | 1  | 1  | 0  | 1  | 1  | 1  | 1  | 1  | 1  | 0  | 0  | 0  | 0  | 0  | 1  | 0  | 0  | 1  | 0  | 1  | 0  | 1  | 1  | 1  | 1  | 0  | 0  | 0  | 0  | 1  | 1  | 0  | 1  | 0  | 1  | 1  | 0  | 1  | 1  | 1  | 0  | 1  | 1  | 1  |

# Rank Index for Bitvector

► Apart from  $B$ , we store:

1. Rank of first pos. of each **mini array** of  $L = \lg^2(n)$  bits

$$\# \text{ mini arrays} = \left\lceil \frac{n}{L} \right\rceil = O\left(\frac{n}{\lg^2 n}\right) \rightarrow \text{can store } O(\lg^2 n) \text{ bits each}$$



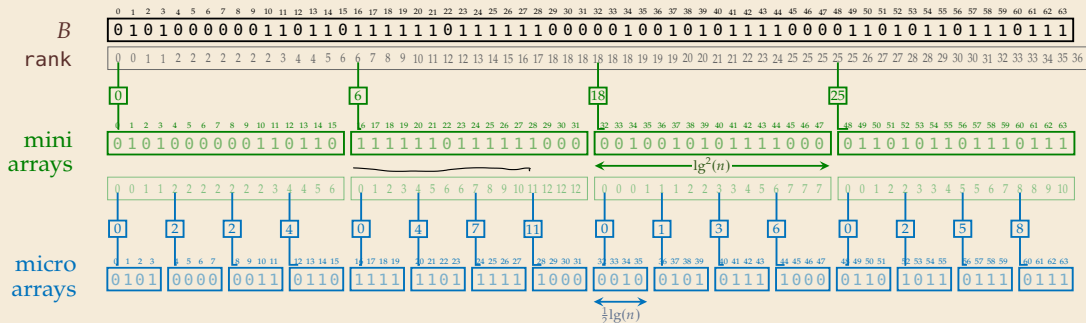


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1. Rank of first pos. of each **mini array** of  $L = \lg^2(n)$  bits  $\rightsquigarrow \frac{n}{L} \cdot \lg(n) = \frac{n}{\lg n} = o(n)$  bits
2. Rank of first pos. in **micro array** of  $\ell = \frac{1}{2} \lg(n)$  bits *relative to its mini array*

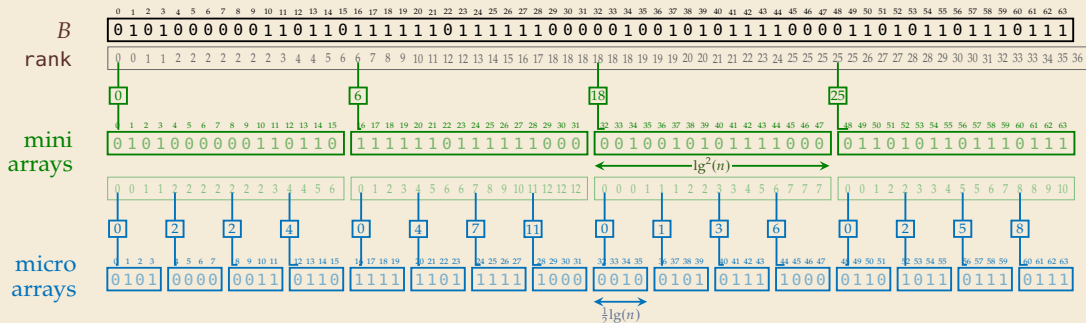
# micro arrays  $O\left(\frac{n}{\lg^2 n}\right)$   
 # 1s in mini array  $\leq L$



# Rank Index for Bitvector

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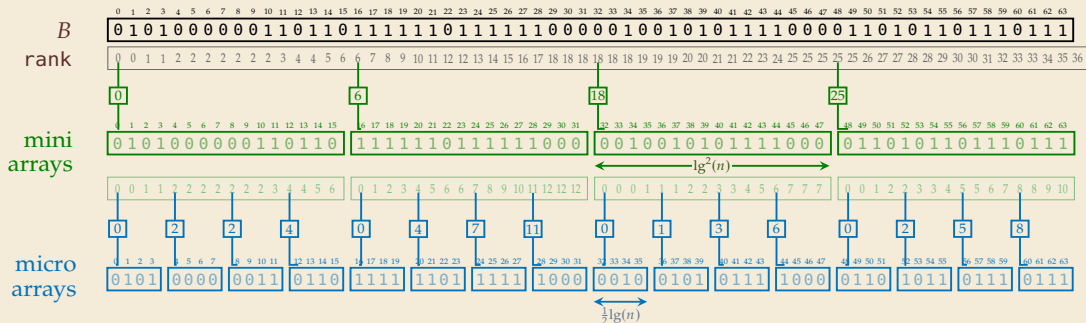
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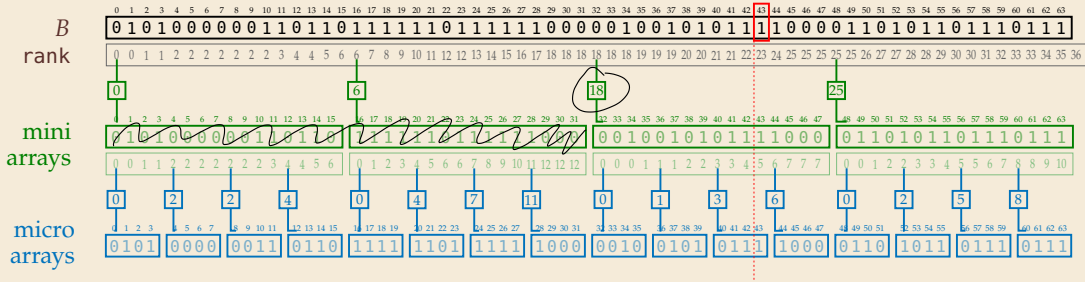
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$\rightsquigarrow \frac{n}{\ell} \lg L = \frac{n}{\frac{1}{2} \lg n} \cdot 2 \lg \lg n = \frac{4n \lg \lg n}{\lg n} = o(n)$  bits



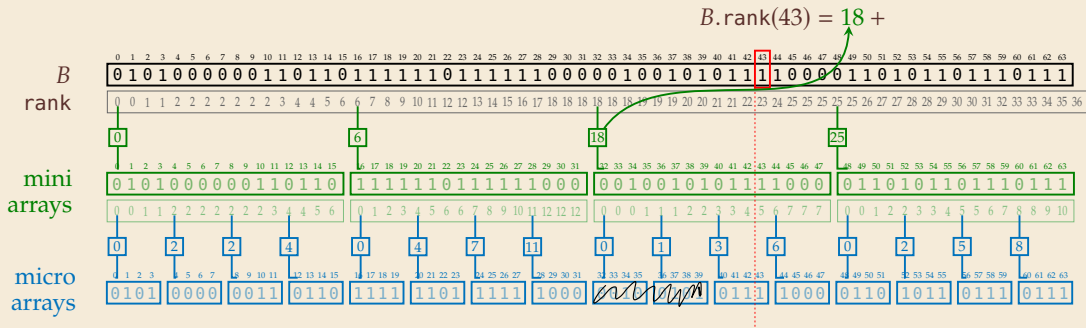
# How to find rank

$$B.\text{rank}(43) =$$



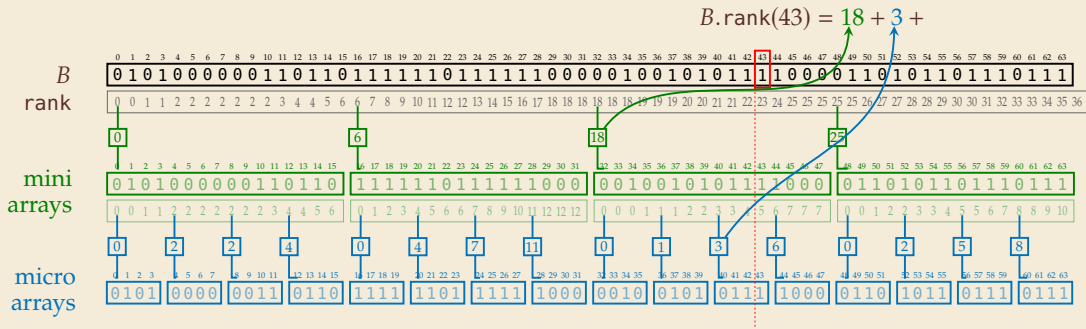
► How to compute  $B.\text{rank}(i)$ ?

# How to find rank



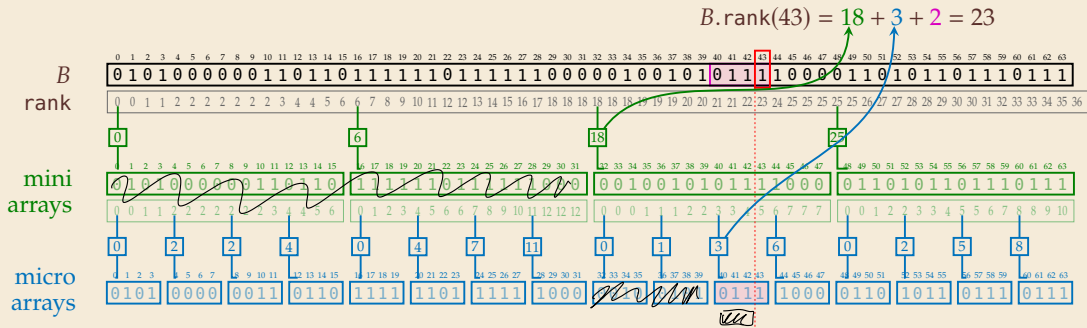
- ▶ How to compute  $B.rank(i)$ ?
  - ▶ find rank up to element's mini array

# How to find rank



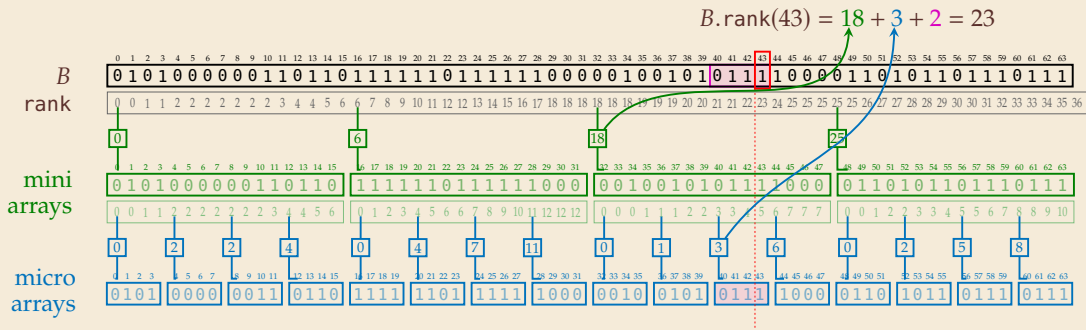
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  - ▶ add rank up to element's micro array (mini-array local)

# How to find rank



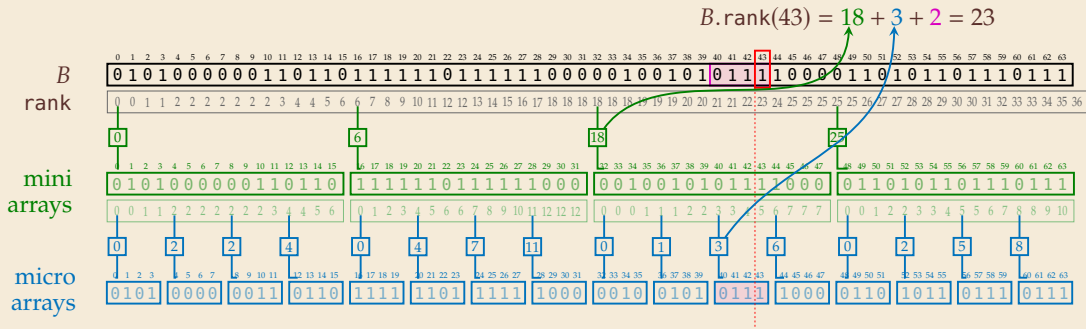
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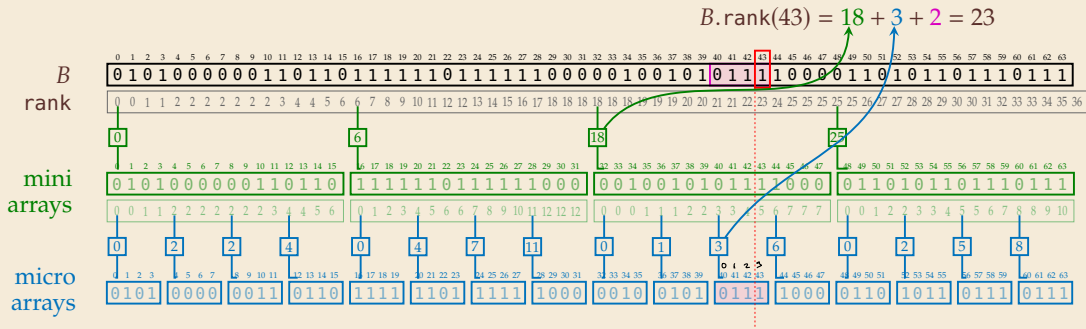
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  - can either do this naively by scanning  $\frac{1}{2} \lg n$  bits  $\rightsquigarrow O(\log n)$  time
  - or use bit marks and pop-count instructions on CPUs  $\rightsquigarrow O(1)$  time
  - or use exhaustive *lookup table!*  $\frac{1}{2} \lg n$  bits  $\rightsquigarrow$  only  $2^{1/2 \lg n} = \sqrt{n}$  different micro arrays

# Exhaustive Tabulation

Classic word-RAM technique: bootstrap on small cases with *exhaustive tabulation*

- ▶ **Idea 1:** on word-RAM, can use integers (and hence small bit sequences) as array index
- ▶ **Idea 2:** for micro subproblems on tiny bit sequences, cannot have many *different* micro subproblems
- ↪ Precompute all micro subproblems in a lookup table, indexed by the subproblem's bitpattern
- ↪  $O(1)$  time for micro subproblem (after preprocessing)

## Back to Rank!

**Recall:** Our task is to compute the local rank in a micro array of  $\ell = \frac{1}{2} \lg n$  bits.

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**Preprocessing:** Build table for all possible queries for all *possible* micro arrays

$$\mu R[0..2^\ell][0..\ell) =$$

| idx | micro array | rank(0) | rank(1) | rank(2) | rank(3) | rank(4) |
|-----|-------------|---------|---------|---------|---------|---------|
| 0   | 0000        | 0       | 0       | 0       | 0       | 0       |
| 1   | 0001        | 0       | 0       | 0       | 0       | 1       |
| 2   | 0010        | 0       | 0       | 0       | 1       | 1       |
| 3   | 0011        | 0       | 0       | 0       | 1       | 2       |
| 4   | 0100        | 0       | 0       | 1       | 1       | 1       |
| 5   | 0101        | 0       | 0       | 1       | 1       | 2       |
| 6   | 0110        | 0       | 0       | 1       | 2       | 2       |
| 7   | 0111        | 0       | 0       | 1       | 2       | 3       |
| 8   | 1000        | 0       | 1       | 1       | 1       | 1       |
| 9   | 1001        | 0       | 1       | 1       | 1       | 2       |
| 10  | 1010        | 0       | 1       | 1       | 2       | 2       |
| 11  | 1011        | 0       | 1       | 1       | 2       | 3       |
| 12  | 1100        | 0       | 1       | 2       | 2       | 2       |
| 13  | 1101        | 0       | 1       | 2       | 2       | 3       |
| 14  | 1110        | 0       | 1       | 3       | 3       | 3       |
| 15  | 1111        | 0       | 1       | 2       | 3       | 4       |

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Space:  $2^\ell \cdot \ell \cdot \lg \ell = \sqrt{n} \cdot \text{polylog}(n) = o(n)$

$\ell$  rows  $\ell$  cols

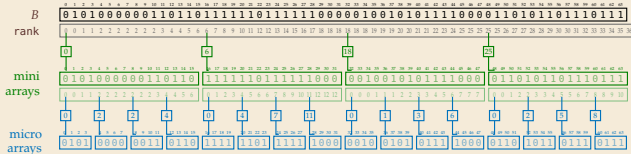
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► Note:  $\#\text{micro arrays} = \frac{n}{\frac{1}{2} \lg n} \gg$

$\#\text{different micro arrays} = 2^{1/2 \lg n} = \sqrt{n}$

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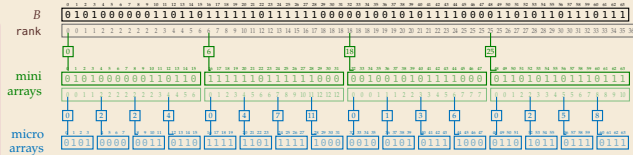
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► Note:  $\# \text{micro arrays} = \frac{n}{\frac{1}{2} \lg n} \gg$

$$\# \text{different micro arrays} = 2^{1/2 \lg n} = \sqrt{n}$$

► To access micro-array  $\text{rank}(i)$

1. Read micro array type (bits), here from  $B$
2. Treat type as binary number  $\rightsquigarrow$   $\text{idx } t$
3. Return  $\mu R[t][i]$

Space:  $2^\ell \cdot \ell \cdot \lg \ell = \sqrt{n} \cdot \text{polylog}(n) = o(n)$

# Succinct Rank – Result

We have shown:

## Theorem 6.1 (Succinct bitvector with rank)

Given a bitvector  $B[0..n)$ , we can construct in  $O(n)$  time an index data structure using

$O\left(n \frac{\lg \lg n}{\lg n}\right) = o(n)$  bits of space that can answer  $B.\text{rank}(i)$  queries in  $O(1)$ .

The index requires oracle access to  $B$  at query time, i. e., in  $O(1)$  time we must be able to read  $\Theta(\log n)$  consecutive bits of  $B$ . ◀

$T[0..n) =$ 

|   |   |   |   |   |   |   |   |   |   |    |    |    |    |    |    |    |
|---|---|---|---|---|---|---|---|---|---|----|----|----|----|----|----|----|
| 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 10 | 11 | 12 | 13 | 14 | 15 | 16 |
| a | b | a | n | a | n | a | a | n | d | a  | n  | a  | a  | p  | l  | e  |

$C[0..m) =$ 

|   |   |   |   |   |   |   |   |   |   |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |   |
|---|---|---|---|---|---|---|---|---|---|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|---|
| 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 10 | 11 | 12 | 13 | 14 | 15 | 16 | 17 | 18 | 19 | 20 | 21 | 22 | 23 | 24 | 25 | 26 | 27 | 28 | 29 | 30 | 31 | 32 | 33 | 34 | 35 | 36 | 37 | 38 | 39 | 40 |   |
| 0 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 1 | 0 | 0  | 1  | 0  | 0  | 0  | 1  | 0  | 1  | 1  | 1  | 0  | 1  | 0  | 0  | 1  | 1  | 0  | 0  | 1  | 1  | 0  | 1  | 1  | 1  | 1  | 1  | 1  | 1  | 1  | 1  | 1  | 0 |

$S[0..m) =$  11000s11011011101000011011001001000010200

$S.\text{select}(v)$       recompute  $S$  from  $C$  even on demand

## Four Russians?

The *exhaustive-tabulation technique* is often called “Four Russians trick” in the literature . . .

- ▶ The algorithmic technique was published 1970 by V. L. Arlazarov, E. A. Dinitz, M. A. Kronrod, and I. A. Faradžev



**Arlazarov, Dinitz, Kronrod, Faradžev:** *On economical construction of the transitive closure of a directed graph*, Dokl. Akad. Nauk SSSR 1970

inofficial translation: <https://minorfree.github.io/FourRussian/>

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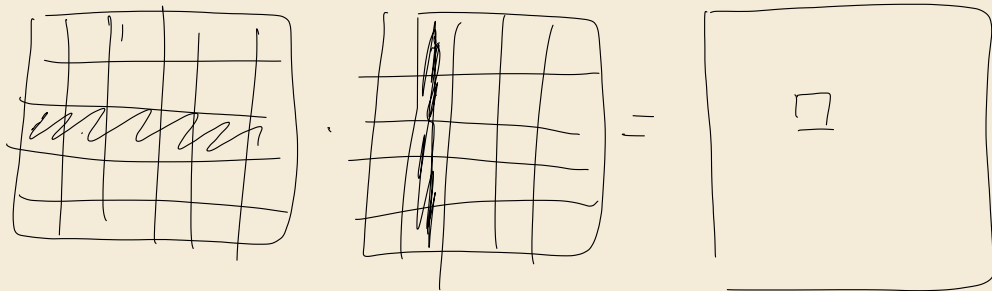
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. . . name in widespread use

# The Original Application: Boolean Matrix Multiplication

Suppose we want to multiply two  $n \times n$  Boolean matrices  $C = A \cdot B$ .

We divide  $A$ ,  $B$ , and  $C$  into  $\ell \times \ell$  *micro matrices*.

$\rightsquigarrow$   $C$  consists of  $\left(\frac{n}{\ell}\right)^2$  micro matrices, each of which is the sum of  $\frac{n}{\ell}$  micro-matrix products.



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The number of *different* possible micro matrix products is  $L = 2^{\ell^2} \cdot 2^{\ell^2}$ .

If we pick  $\ell = \frac{1}{4}\sqrt{\lg n}$ , we have only  $L = 2^{2\ell^2} = \sqrt{n}$  different products.

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For two micro matrices  $a$  and  $b$ , we store  $a \cdot b$  at the offset  $a_{1,1} \dots a_{\ell,\ell} b_{1,1} \dots b_{\ell,\ell}$ , where we interpret this bitstring as a binary number.

On a word RAM, we can use this as indirect memory access in  $O(1)$  time.

↪ Any micro matrix sum/product takes  $O(1)$  time after a total of  $O(\sqrt{n} \cdot \log^{3/2} n)$  preprocessing.

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The total time to compute one micro matrix in  $C$  is thus  $O(\frac{n}{\ell})$ .

So the total time to compute  $C$  is  $O(n^3/\ell^3) = O(n^3/\log^{3/2} n)$ .

# The Original Application: Boolean Matrix Multiplication

Suppose we want to multiply two  $n \times n$  Boolean matrices  $C = A \cdot B$ .

We divide  $A$ ,  $B$ , and  $C$  into  $\ell \times \ell$  *micro matrices*.

↪  $C$  consists of  $(\frac{n}{\ell})^2$  micro matrices, each of which is the sum of  $\frac{n}{\ell}$  micro-matrix products.

The number of *different* possible micro matrix products is  $L = 2^{\ell^2} \cdot 2^{\ell^2}$ .

If we pick  $\ell = \frac{1}{4}\sqrt{\lg n}$ , we have only  $L = 2^{2\ell^2} = \sqrt{n}$  different products.

↪ **Exhaustive Tabulation:** Can *precompute* all  $\sqrt{n}$  possible micro-matrix sums/products!

For two micro matrices  $a$  and  $b$ , we store  $a \cdot b$  at the offset  $a_{1,1} \dots a_{\ell,\ell} b_{1,1} \dots b_{\ell,\ell}$ , where we interpret this bitstring as a binary number.

On a word RAM, we can use this as indirect memory access in  $O(1)$  time.

↪ Any micro matrix sum/product takes  $O(1)$  time  
after a total of  $O(\sqrt{n} \cdot \log^{3/2} n)$  preprocessing.

The total time to compute one micro matrix in  $C$  is thus  $O(\frac{n}{\ell})$ .

So the total time to compute  $C$  is  $O(n^3/\ell^3) = O(n^3/\log^{3/2} n)$ .

**Note:** By taking  $n \times \ell$  resp.  $\ell \times n$  “*micro strips*” instead of squares, we can choose  $\ell = \Theta(\log n)$  and obtain final time  $O(n^3/\log^2 n)$ .

## 6.4 Supporting Select

## Select Indices – Simple Ideas

Recall:  $B.\text{select}_1(r) = \min\{j : B.\text{rank}(j+1) \geq r\} \cup \{n\}$   
= index of  $r$ th 1 in  $B$ , ( $r = 1, 2, \dots$ )

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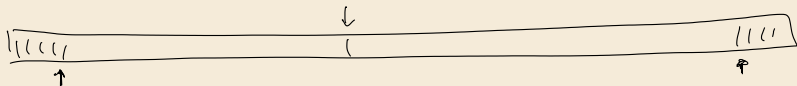
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▶ hybrid solution by subsampling:

▶ explicitly store every  $k$ th select answer, binary search in between

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*Can we do better?*

# Select Index

$$O\left(n \frac{\lg^2 n}{\sqrt{\lg n}}\right)$$

## Theorem 6.2 (Succinct bitvector with select)

Given a bitvector  $B[0..n]$ , we can construct in  $O(n)$  time an index data structure using

$$O\left(n \frac{(\lg \lg n)^2}{\lg n}\right) = o(n) \text{ bits of space that can answer } B.\text{select}(r) \text{ queries in } O(1).$$

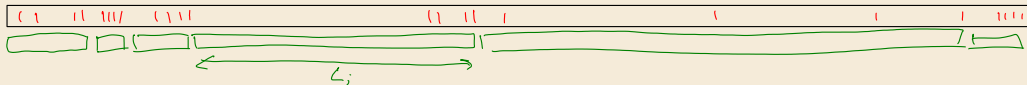
The index requires oracle access to  $B$  at query time, i. e., in  $O(1)$  time we must be able to read  $\Theta(\log n)$  consecutive bits of  $B$ . ◀

try as w/ rank?



$$\text{chunk id} = \lg(\# \text{chunks}) = \lg\left(\frac{n}{L}\right) = \lg n - \lg L$$

Proof:



Split bitvector into miniblocks of  $k$  ones each

$$k = \lg^2 n$$

$$\Rightarrow \# \text{miniblocks} = O\left(\frac{n}{k}\right)$$

$r$ th one is in  $\lfloor \frac{r}{k} \rfloor$ th mini block.

Case distinction

• Long miniblock  $L_j > \lg^4(n)$

$\Rightarrow$  have  $\leq \frac{n}{\lg^4(n)}$  such long miniblocks each w/  $k$  ones

$\Rightarrow$  store indices of  $k$  ones explicitly

$$\frac{n}{\lg^4(n)} \cdot \lg^2(n) \cdot \lg(n) = O\left(\frac{n}{\lg(n)}\right) = o(n)$$

• Dense miniblock ( $L_j \leq \lg^4(n)$ ) store starting index of mini block

$\rightarrow$  can write mini block-local index w/  $O(\lg \lg n)$  bits still too much

recursively subdivide into micro blocks w/  $k$  ones

$$k = \sqrt{\lg n}$$

$$\text{micro block} = \left\lfloor \frac{r'}{k} \right\rfloor$$

$$r' = r - \left\lfloor \frac{r}{k} \right\rfloor$$

second-level case distinction

• long micro block  $l_j > \frac{1}{2} l_{gn}$

only  $\leq \frac{2n}{l_{gn}}$  such long micro blocks

$\Rightarrow$  write down indices of ones

$$\frac{2n}{l_{gn}} \cdot k \cdot O(l_{gn} l_{gn}) = O\left(\frac{n l_{gn} l_{gn}(k)}{\sqrt{l_{gn}(k)}}\right) = o(n)$$

• dense micro block store mini-block-local starting index

$l_j \leq \frac{1}{2} l_{gn} \Rightarrow$  can use exhaustive table lookup

to answer micro-block-local select

Remarks:

a more complicated index achieves rank & select  
with  $O(n \frac{\lg \lg n}{\lg n})$  bits of extra space.

also: for GC1 rank/select in indexing model

we need  $\Omega(n \frac{\lg \lg n}{\lg n})$  bits of extra space.

## 6.5 Compressed Bitvectors

## Recall: Bitvector Applications

- ▶ Many applications of bitvectors naturally yield *unbalanced* bitvectors:
  - ▶ characteristic vector of set  $S \subseteq [0..u)$   $\rightsquigarrow$   $n$  ones among  $u$  bits
  - ▶ prefix sums via unary encoding  $\rightsquigarrow$  #zeros = #numbers; #ones = total sum
  - ▶ sparse array index  $\rightsquigarrow$  only useful if many fewer ones than entries